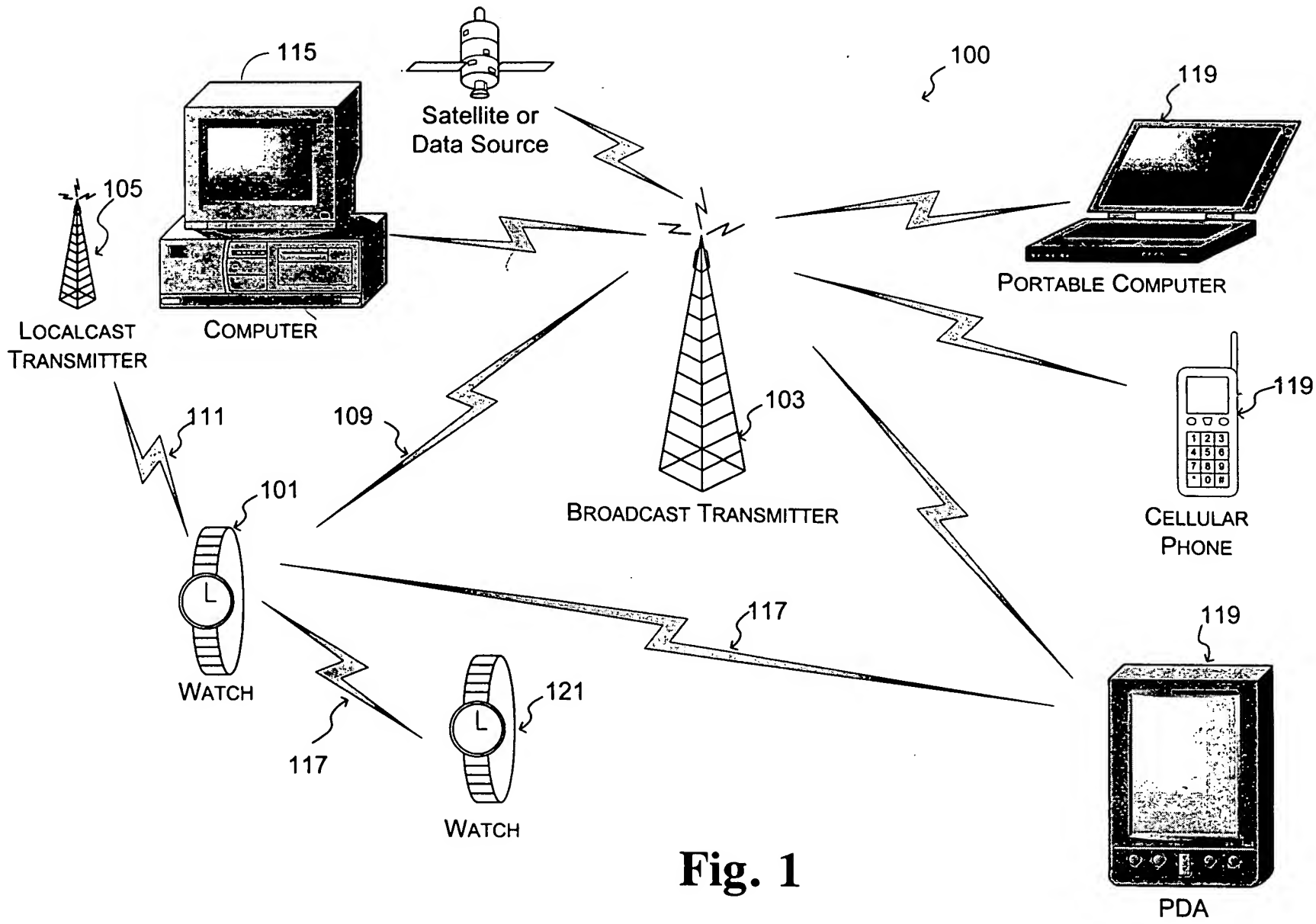

Fig. 1

APPROVED O.G. FIG.
CLASS SUBCLASS
DRAFTSMAN

APPROVED | C. G. FIG.
BY | CLASS | SUBCLASS

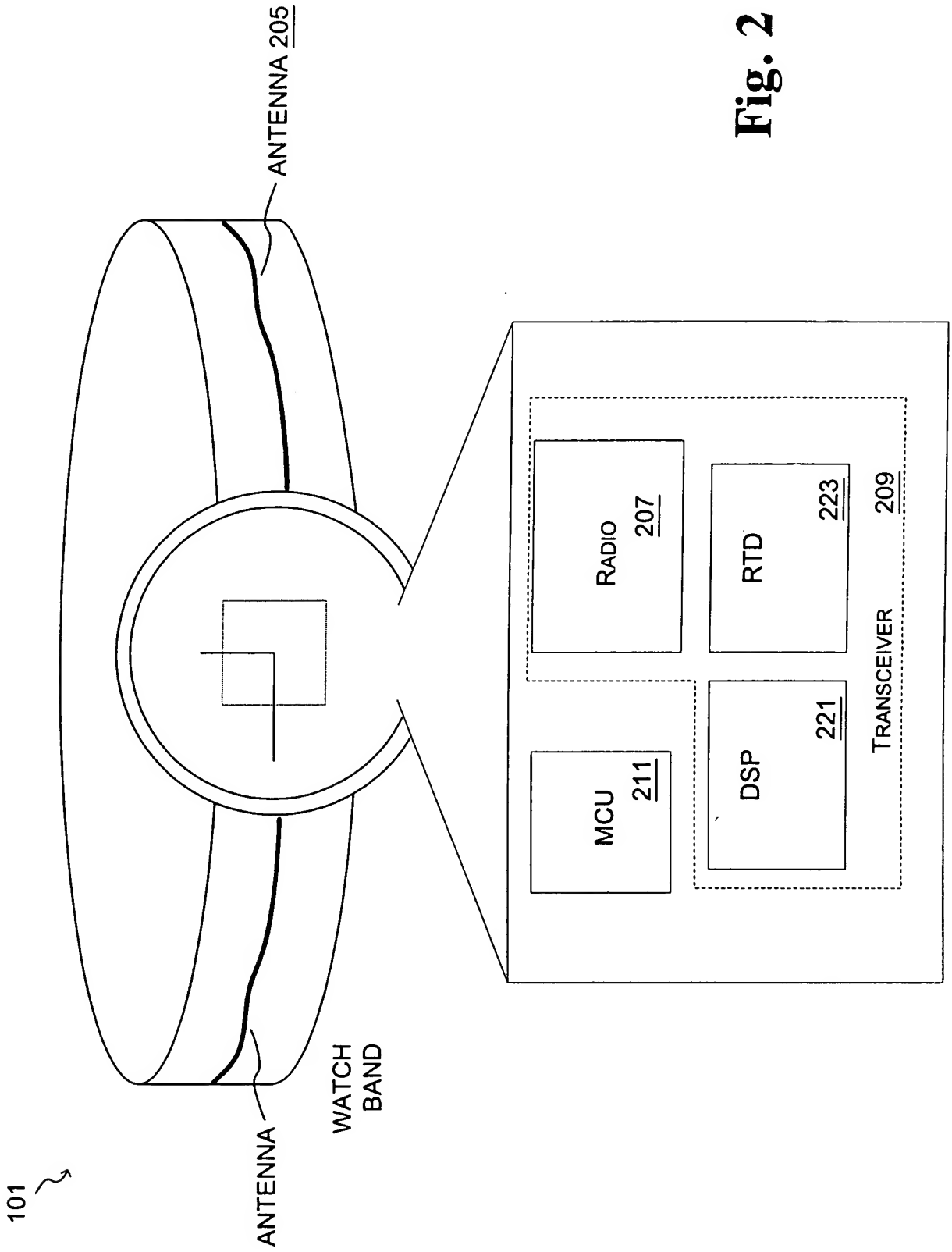

Fig. 2

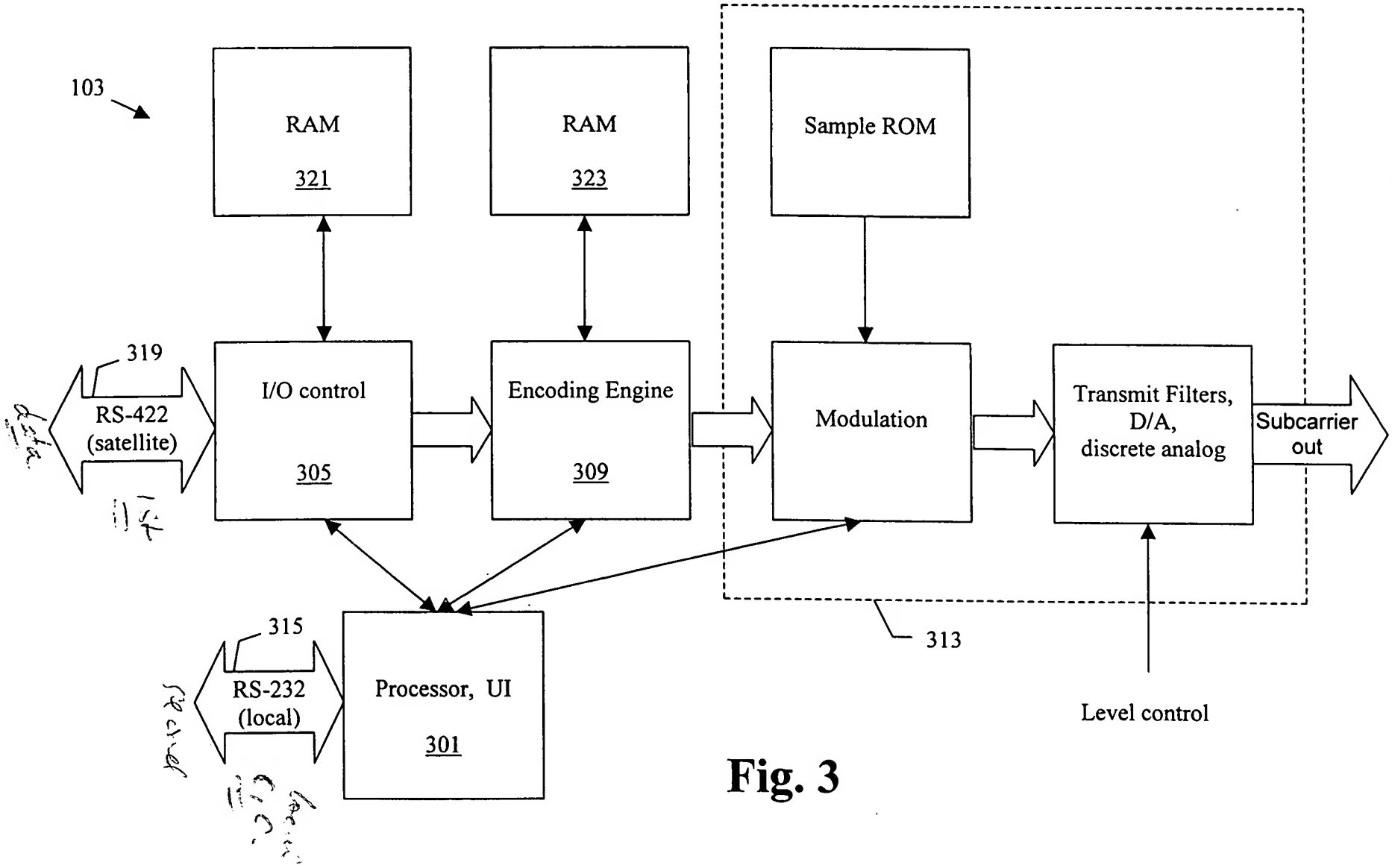
103
RAM
321
RAM
323
Sample ROM
319
RS-422
(satellite)
I/O control
305
Encoding Engine
309
Modulation
Transmit Filters,
D/A,
discrete analog
Subcarrier
out
313
315
RS-232
(local)
Processor, UI
301
Level control

Fig. 3

APPROVED O.G. FIG.
BY CLASS SUBCLASS
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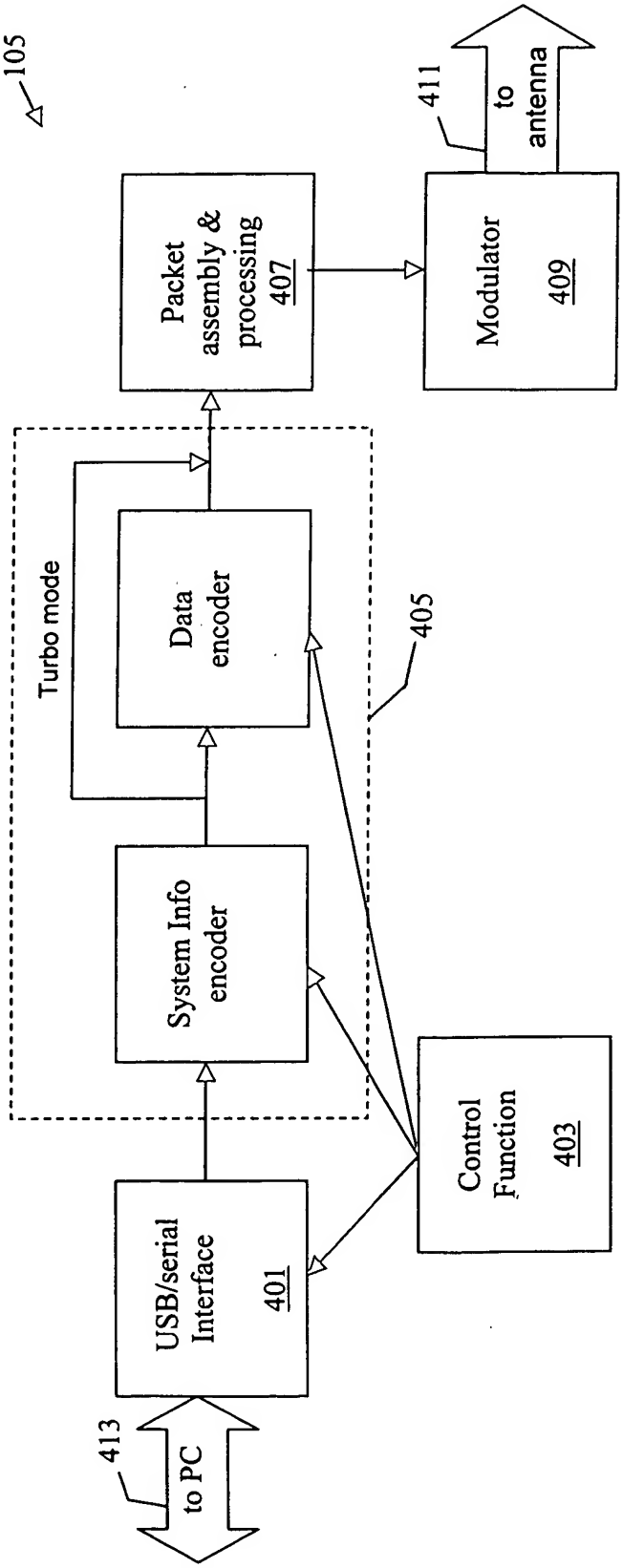

Fig. 4

APPROVED O.G. FIG.
CLASS SUBCLASS

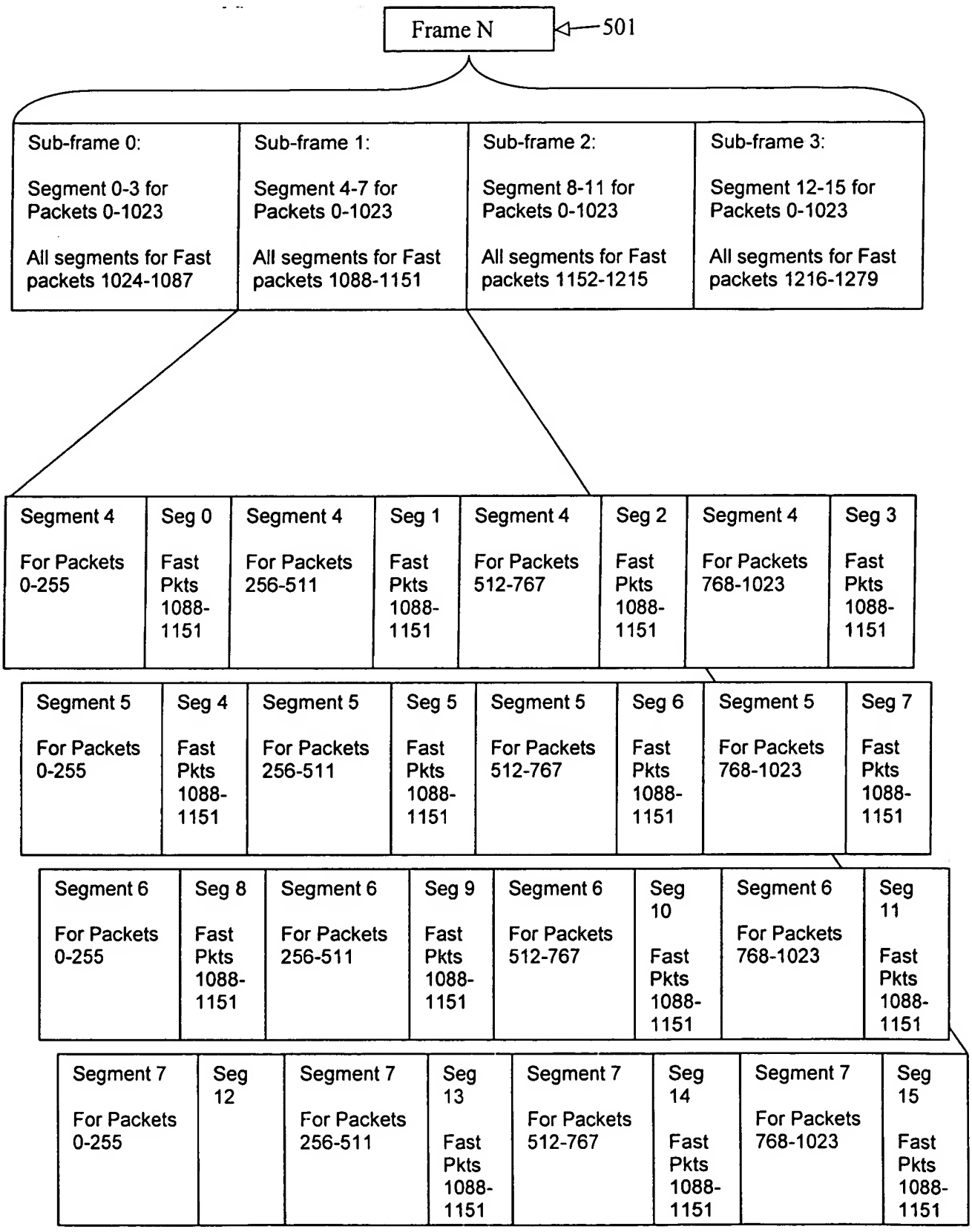

Fig. 5

APPROVED | C. G. FIG.
BY | CLASS | SUBCLASS
DRAFTSMAN

MODIFIED HDLC FRAME FORMAT

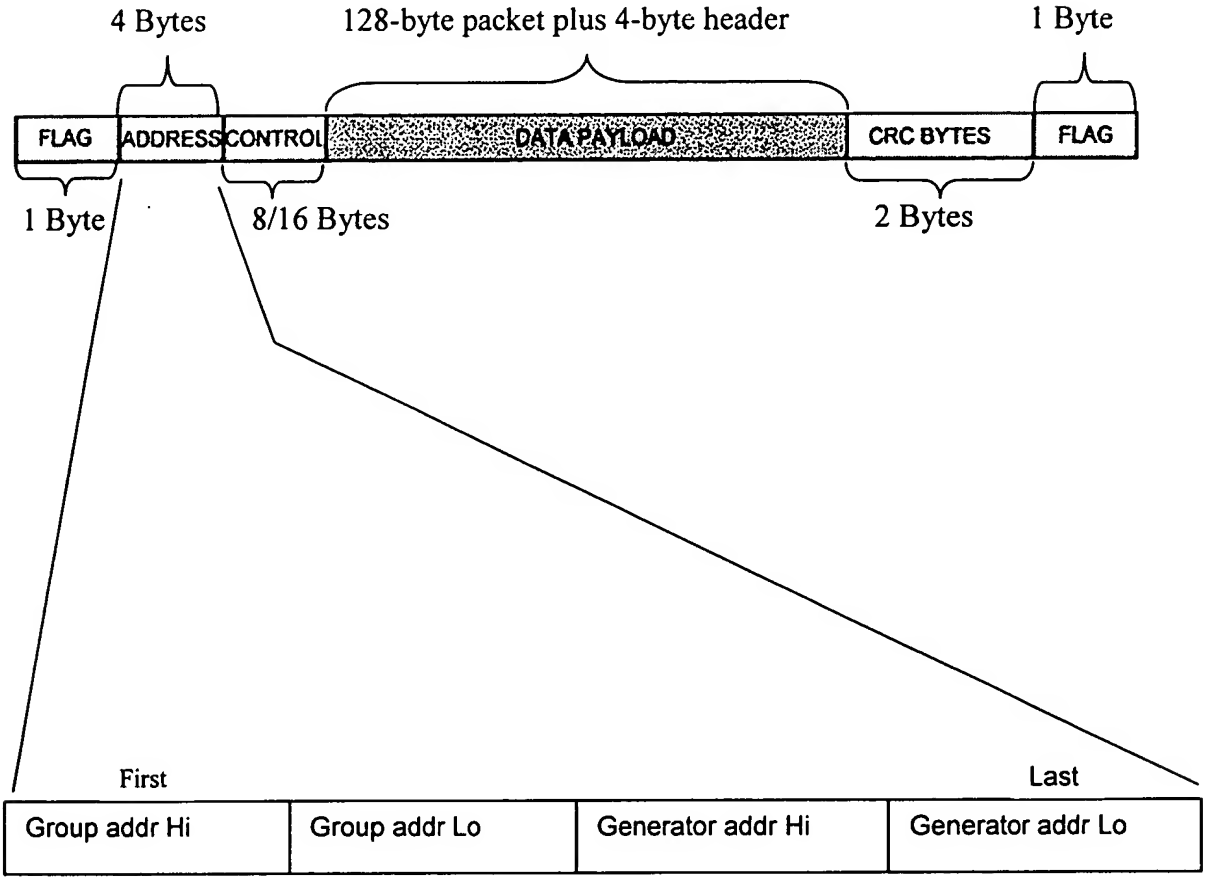

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Fig. 6

APPROVED O.G. FIG.
CLASS SUBCLASS

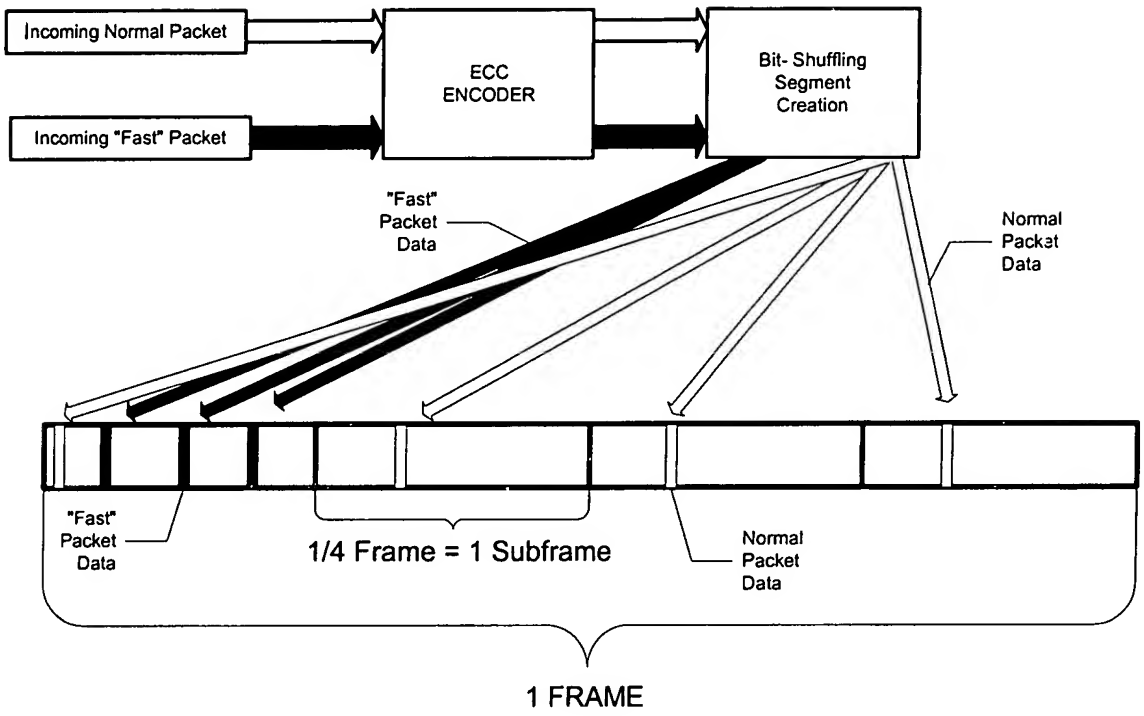


Fig. 7

APPROVED O.G. FIG.
BY CLASS SUBCLASS
DRAFTSMAN

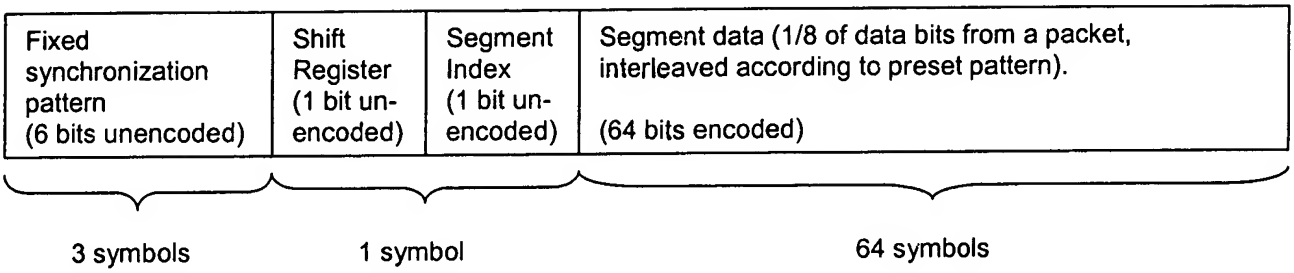
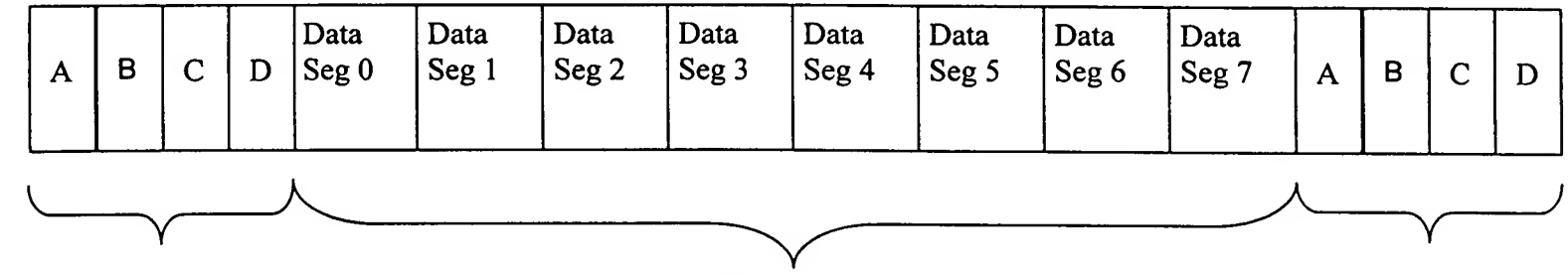


Fig. 8

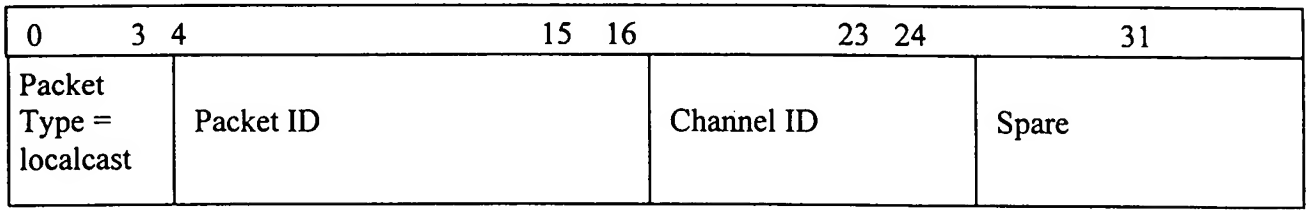
APPROVED FOR PUBLICATION
BY
CLASS/SUBCLASS:



Correlation sequence 64 symbols

Eight segments, each of 64 data bits, interleaved, and 4 system information bits, separately ½-rate encoded. (4*8*2)+(64*8*2) = 1088 FM symbols/packet

Correlation sequence 64 symbols



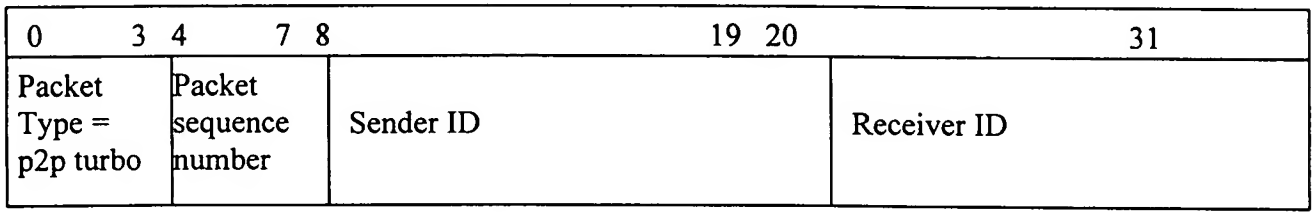


Fig. 9

APPROVED O.G. FIG.
BY CLASS SUBCLASS

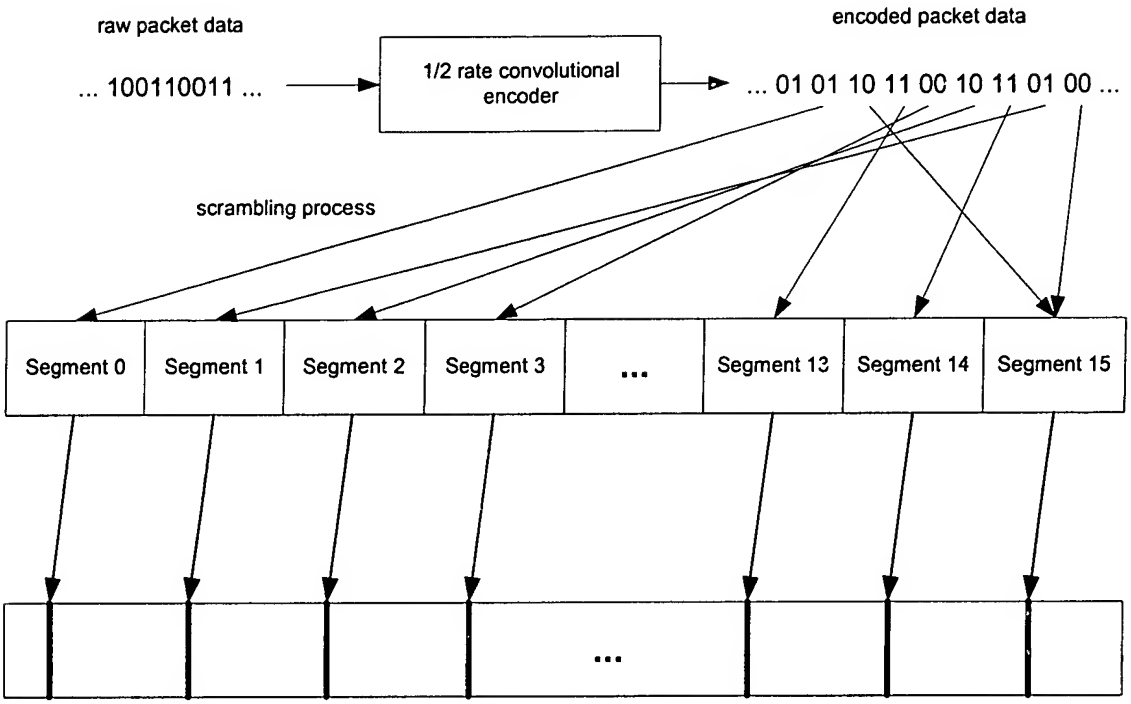


Fig. 10

APPROVED
DY
DRAFTSMAN
C. G. FIG.
CLASS SUBCLASS

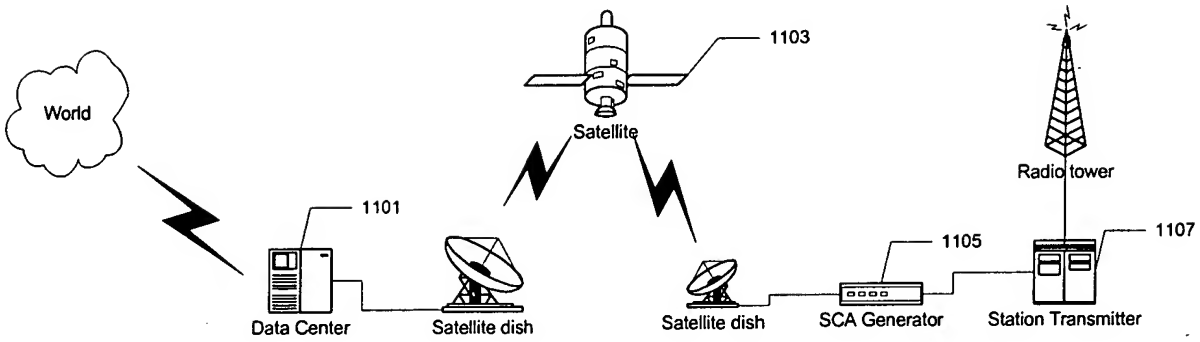


Fig. 11

APPROVED O.G. FIG.
BY CLASS SUBCLASS
DRAFTSMAN

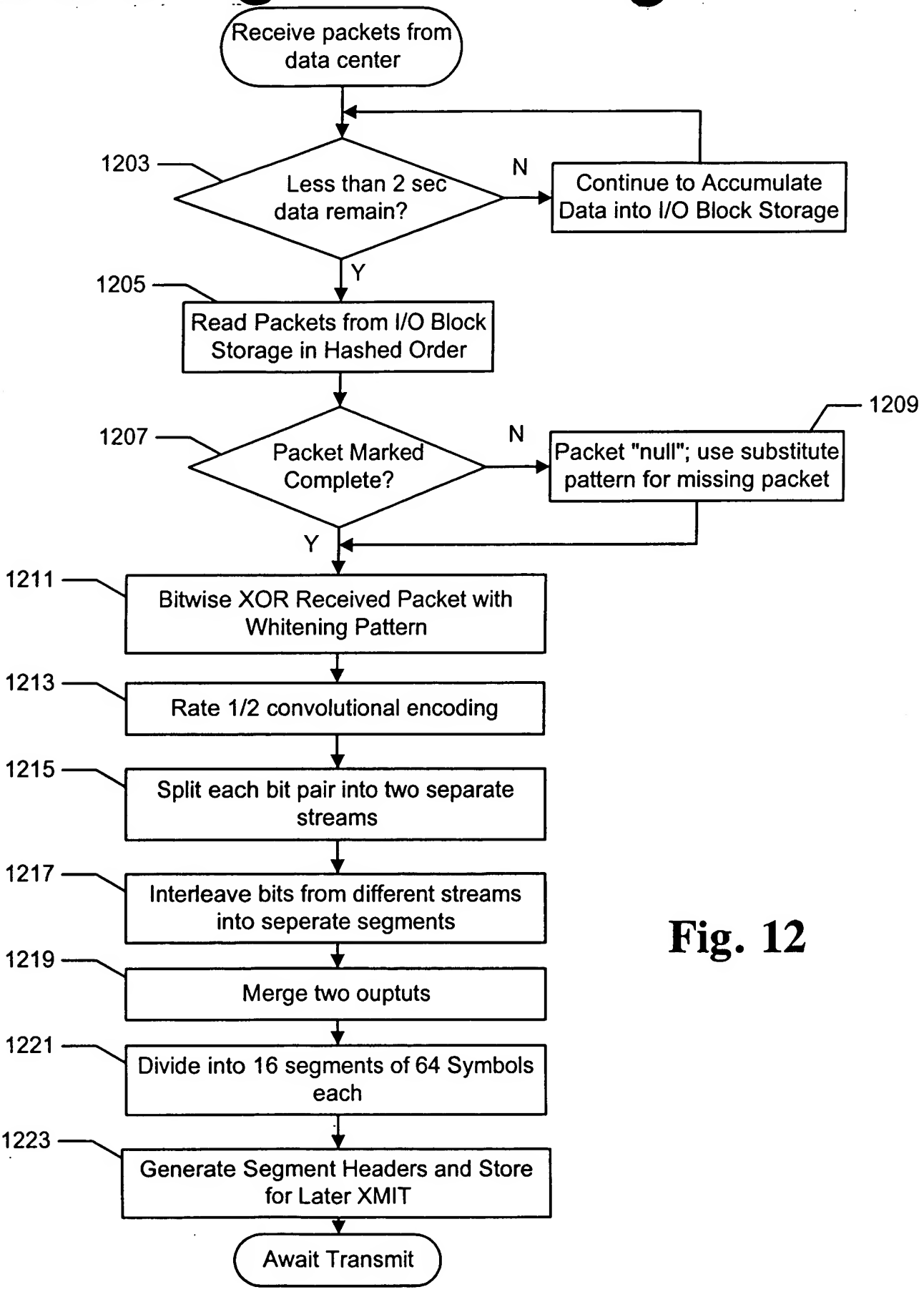


Fig. 12